ABSTRACT

A pedestal enclosure for electronic components is provided. The pedestal enclosure includes a base section and a cover engageable with the base section so as to define an interior space. The base section has a generally rectangular configuration and having a plurality of longitudinally extending channels formed therein such that the base section is substantially self-supporting in the ground. The cover has a generally cylindrical configuration and is engageable with a cylindrical neck on the base section so as to define an interior space. A bracket system is supported on the base section and arranged in the interior space. The bracket system includes a pair of legs. The base section comprises front and rear housing sections that can be selectively assembled together and split apart without removal of the bracket system. A lock mechanism is arranged near the top or near the bottom of the cover. A first lock receptacle is arranged on the base section for receiving the lock mechanism when the cover is engaged with the base section and the lock mechanism is arranged near the bottom of the cover and a second lock receptacle is arranged on the bracket system for receiving the lock mechanism when the cover is engaged with the base section and the lock mechanism is arranged near the top of the cover. A splice bar is pivotably supported between the legs of the bracket system for movement between an locked position wherein a hook at each end of the splice bar engages a respective one of the legs of the bracket system and an unlocked position wherein the splice bar is disengaged from the legs of the bracket system.